

REMARKS

At the outset, the Examiner is thanked for the thorough review and consideration of the pending application. The Office Action dated March 24, 2006, has been received and its contents carefully reviewed. Applicant would like to thank Examiner Wu for the courtesy shown to Applicant's Representative during the interview on April 19, 2005. During the interview, the Examiner provided further clarification of his response to Applicant's arguments filed in the parent case on March 17, 2004. In addition, as noted in the Interview Summary (Paper No. 20060419), the Examiner agreed that features of the invention described in the Applicant's specification were not taught by the Aoki reference (US Patent 4,755,891).

Claims 1, 2, 5-8, 15-18, 20 and 21 are rejected by the Examiner. Claims 1, 7, 16, 18, and 21 have been amended, and no claims have been canceled. No new matter has been added. Claims 1, 2, 5-8, 15-18, 20 and 21 remain pending in this application.

In the Office Action, claims 1, 2, 5-8, 18-18, 20 and 21 are rejected under 35 U.S.C. § 103(a) as being unpatentable over U.S. Patent No. 4,775,891 to Aoki et al. (hereinafter "Aoki"). Applicant submits that claims 1, 2, 5-8, 18-18, 20 and 21 are allowable over Aoki because Aoki does not teach or suggest every element of the claims. Accordingly, Applicant respectfully traverses the rejection and requests reconsideration.

Claim 1 recites a method of driving a liquid crystal display, including "modulating source data of one frame period using registered data from a frame period previous to the one frame period and supplying the modulated data to a liquid crystal panel at an initial period of the one frame period." Aoki states, "Meanwhile, A/D converter 3 samples the video signal supplied from the video amplifier in individual horizontal scanning periods d1, d2,...as shown in FIG. 4 for conversion to the 4-bit digital signal O₁-O₄ fed to the data control circuit 4. The data control circuit 4 provides the 3-bit data D₁-D₃ according to the signal O₁-O₄ from the A/D converter 3 and data control signal E from the synchronization control circuit 2. More specifically, the data control circuit 4 provides data D₁ to D₃ corresponding to the data O₁ to O₄ from the A/D converter 3 as different values when the data control signal E is E=0 and E=1, respectively, as shown in FIG. 3." Applicant submits that Aoki does not teach or suggest at least the cited feature of claim 1 and that accordingly claim 1 is allowable over Aoki.

Claim 7 recites an apparatus for driving a liquid crystal display including "a modulator modulating source data of one frame period using registered data from a frame period previous

to the one frame period". Aoki states, "Meanwhile, A/D converter 3 samples the video signal supplied from the video amplifier in individual horizontal scanning periods d1, d2,...as shown in FIG. 4 for conversion to the 4-bit digital signal O₁-O₄ fed to the data control circuit 4. The data control circuit 4 provides the 3-bit data D₁-D₃ according to the signal O₁-O₄ from the A/D converter 3 and data control signal E from the synchronization control circuit 2. More specifically, the data control circuit 4 provides data D₁ to D₃ corresponding to the data O₁ to O₄ from the A/D converter 3 as different values when the data control signal E is E=0 and E=1, respectively, as shown in FIG. 3." Applicant submits that Aoki does not teach or suggest at least the cited feature of claim 7 and that accordingly claim 7 is allowable over Aoki.

Claim 18 recites a liquid crystal display including "a modulator modulating source data of one frame period based on registered data from a frame period previous to the one frame period." Aoki states, "Meanwhile, A/D converter 3 samples the video signal supplied from the video amplifier in individual horizontal scanning periods d1, d2,...as shown in FIG. 4 for conversion to the 4-bit digital signal O₁-O₄ fed to the data control circuit 4. The data control circuit 4 provides the 3-bit data D₁-D₃ according to the signal O₁-O₄ from the A/D converter 3 and data control signal E from the synchronization control circuit 2. More specifically, the data control circuit 4 provides data D₁ to D₃ corresponding to the data O₁ to O₄ from the A/D converter 3 as different values when the data control signal E is E=0 and E=1, respectively, as shown in FIG. 3." Applicant submits that Aoki does not teach or suggest at least "modulating source data of one frame period based on registered data from a frame period previous to the one frame period" as recited in claim 18, and that accordingly claim 18 is allowable over Aoki.

Claim 21 recites a method of driving a liquid crystal display including "applying a modulated data signal to a liquid crystal panel within one frame period ...wherein the modulated data signal depends on data from a frame period previous to the one frame period." Aoki states, "Meanwhile, A/D converter 3 samples the video signal supplied from the video amplifier in individual horizontal scanning periods d1, d2,...as shown in FIG. 4 for conversion to the 4-bit digital signal O₁-O₄ fed to the data control circuit 4. The data control circuit 4 provides the 3-bit data D₁-D₃ according to the signal O₁-O₄ from the A/D converter 3 and data control signal E from the synchronization control circuit 2. More specifically, the data control circuit 4 provides data D₁ to D₃ corresponding to the data O₁ to O₄ from the A/D converter 3 as different values when the data control signal E is E=0 and E=1, respectively, as shown in FIG. 3." Applicant submits that Aoki does not teach or suggest at least "wherein the modulated data signal depends

on data from a frame period previous to the one frame period.” as recited in claim 21, and that accordingly claim 18 is allowable over Aoki.

Applicant notes that claims 2, 5 and 6 depend from claim 1, claims 8, 15-17 depend from claim 7, and claim 20 depends from claim 18. Claims 2, 5-6, 8, 15-17 and 20 each include all of the limitations of their respective base claims. Accordingly, Applicant submits that claims 2, 5-6, 8, 15-17 and 20 are allowable over Aoki at least by way of their dependencies from allowable base claims 1, 8, and 18.

Applicant believes the foregoing amendments and remarks place the application in condition for allowance and early, favorable action is respectfully solicited.

If for any reason the Examiner finds the application other than in condition for allowance, the Examiner is requested to call the undersigned attorney at (202) 496-7500 to discuss the steps necessary for placing the application in condition for allowance. All correspondence should continue to be sent to the below-listed address.

If these papers are not considered timely filed by the Patent and Trademark Office, then a petition is hereby made under 37 C.F.R. § 1.136, and any additional fees required under 37 C.F.R. § 1.136 for any necessary extension of time, or any other fees required to complete the filing of this response, may be charged to Deposit Account No. 50-0911. Please credit any overpayment to deposit Account No. 50-0911. *A duplicate copy of this sheet is enclosed.*

Respectfully submitted,

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